

Form PTO-1449

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Document Number (Optional)

T8mc-01-231

Application Number

10/043, 864

Applicant

K.T. Chen et al.

Filing Date

01/10/02

Group Art Unit

2858

U. S. PATENT DOCUMENTS

EXAMINER INITIAL	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	PUB. DATE & APPROPRIATE
TN	5981971	11/9/99	Miyakawa	257	48	3/13/98
TN	6157046	12/5/00	Corbett et al.	257	48	4/23/99
TN	5835552	11/10/98	Kusumoto et al.	377	24	11/12/96
TN	5818895	10/6/98	Oh	337	118	5/15/96
TN	5619437	4/8/97	Nagai	364	715.09	9/27/95
TN	6064213	5/16/00	Khandros et al.	324	754	1/15/97
TN	6246075	6/12/01	Su et al.	257	48	2/22/00
TN	5946248	8/31/99	Chien et al.	365	201	2/27/98
TN	5995428	11/30/99	Chien et al.	365	201	2/27/98
TN	5057441	10/15/91	Gutt et al.	437	8	10/29/90
TN	5808947	9/15/98	McClure	365	201	9/17/96

FOREIGN PATENT DOCUMENTS

DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	Translation
					YES NO

OTHER DOCUMENTS (Including Author, Title, Date, Portion of Pages, Etc.)

TN	Hodges et al., "Analysis and Design of Digital Integrated Circuits", second edition, David A. Hodges & Horace G. Jackson, McGraw-Hill, New York, Chinese Edition, c. 1983, pp. 371-375.

EXAMINER

DATE CONSIDERED

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